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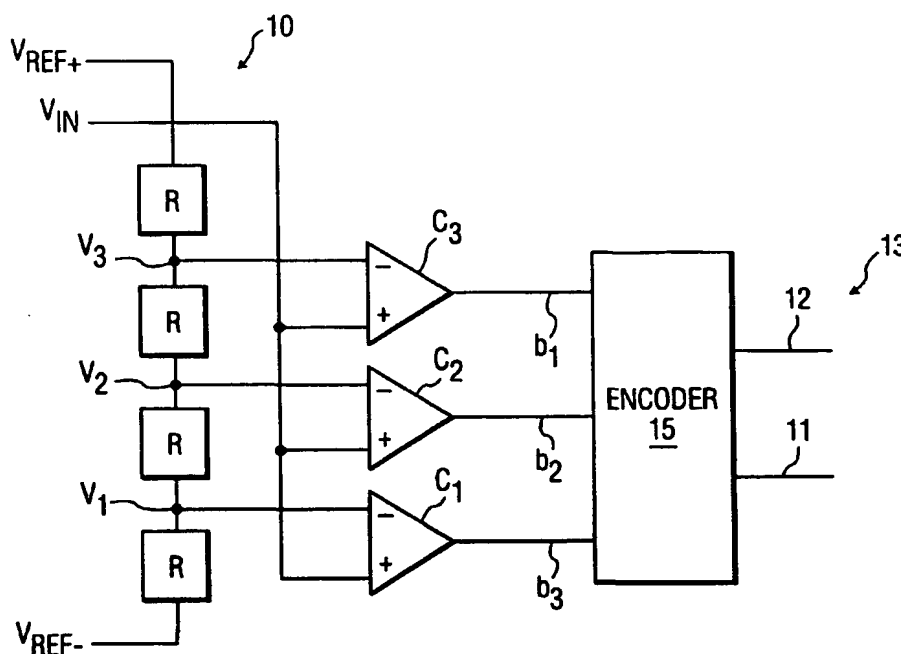
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**Declaration under Rule 4.17:**

— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for all designations

[Continued on next page]

(54) Title: **NON-LINEAR DISTRIBUTION OF VOLTAGE STEPS IN FLASH-TYPE A/D CONVERTERS**



(57) Abstract: A method, apparatus, and system for converting an input voltage  $V_{IN}$  to a digital output. A comparison of  $V_{IN}$  with reference voltages in one or more flash-type analog-to-digital (A/D) converters generates the digital output representing  $V_{IN}$ . If one A/D converter is used, the A/D converter is non-linear. If more than one A/D converter are used, the A/D converters are each linear.

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